

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Analiantian Carial Na	00/024 000
Application Serial No	
Filing Date	April 12, 2001
Inventor	
Assignee	Micron Technology, Inc.
Group Art Unit	
Examiner	L. Schillinger
Attorney's Docket No.	MI22-1637
Title: Semiconductor Processing Methods Of Forming	Transistors, Semiconductor
Processing Methods Of Forming Dynamic Rando	m Access Memory Circuitry,
And Related Integrated Circuitry	•

RESPONSE TO JUNE 2, 2004 OFFICE ACTION

To:

Mail Stop Amendments

Assistant Commissioner for Patents

P.O. Box 1450

Alexandria VA 22313-1450

Ey372467966

From:

James D. Shaurette

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Wells St. John P.S.

601 W. First Avenue, Suite 1300 Spokane, WA 99201-3817

Sir:

Please enter the following amendments prior to examining the above identified application:

<u>AMENDMENTS</u>